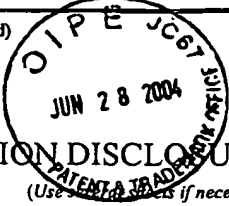


Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Arty. Docket No. Intel/17880	Serial No. 10/747,764
 <p>INFORMATION DISCLOSURE STATEMENT (Use separate sheets if necessary)</p>		Applicants Patel et al.	
		Filing Date December 29, 2003	Group Art Unit 2189 2189

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
<i>R/E</i>	C01	<i>P6 Family of Processors: Hardware Developer's Manual, Intel Corporation, September 1998, 16 pages</i>
<i>R/E</i>	C02	<i>IS 32 Intel Architecture Software Developer's Manual, Volume 3: System Programming Guide, Intel Corporation, 2003, Chapter 2: Sytem Architecture Overview, pp. 2.1-2.7</i>
<i>R/E</i>	C03	<i>IS 32 Intel Architecture Software Developer's Manual, Volume 3: System Programming Guide, Intel Corporation, 2003, Chapter 3: Protected-Mode Memory Management, pp. 3.1-3.38</i>
<i>R/E</i>	C04	<i>IA-32 Intel® Architecture Optimization: Reference Manual, Intel Corporation, 2003, Chapter 1: IA-32 Intel® Architecture Processor Family Overview, pp. 1.1- 1-34</i>
<i>R/E</i>	C05	<i>IA-32 Intel® Architecture Software Developer's Manual, Volume 1: Basic Architecture, Intel Corporation, 2003, Chapter 3: Basic Execution Environment, pp. 3.1-3.17</i>

EXAMINER <i>R. Elmore</i>	DATE CONSIDERED <i>6-3-2006</i>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.</p>	